

In the Claims

1.-23. (Cancelled)

24. (Currently Amended) A process for transmitting data on an optical fiber comprising:

- wavelength division multiplexing ~~in-wavelength~~ of signals coming from a plurality of monochrome transmitters, each of which has its own wavelength and a slave local clock,
 - modulating information to be transmitted by a carrier realized per channel, and
 - reformatting a non-return to zero (NRZ) formatted, multiplexed signal to a return to zero (RZ) multiplexed signal or to carrier suppression return to zero (CS-RZ) multiplexed signal with an optical gate comprising an electro-optical modulator and a clock,
- wherein said reformatting is common and simultaneous for all carriers in the multiplexed signal, and

wherein each slave local clock from each transmitter is controlled by a synchronization circuit comprising a master clock and a phase locked loop (PLL), said master clock controlling the clock of said optical gate and each slave local clock by using said phase locked loop which supplies the synchronization signal for each of the transmitters.

25. (Cancelled)

26. (Previously Presented) The process according to claim 25, wherein the reformatting comprises optimizing the form of the signal as a function of characteristics of propagation of an associated transport means.

27. (Previously Presented) The process according to claim 25, wherein the reformatting comprises optimizing optical parameters of the signal as a function of the characteristics of propagation of an associated transport means.

28. (Previously Presented) The process according to claim 25, wherein the reformatting comprises an operation of stabilizing temporal parameters of data.

29. (Previously Presented) The process according to claim 24, comprising synchronizing streams (pulses) emitted by the transmitters.

30. (Previously Presented) The process according to claim 24, wherein the reformatting comprises aligning the phase of signals generated by the transmitters.

31. (Previously Presented) The process according to claim 30, wherein the aligning is subject to ambient parameters to compensate for temporal signal variations.

32. (Previously Presented) The process according to claim 30, wherein the aligning is subject to ambient parameters to compensate for differences and variations between optical paths.

33. (Previously Presented) The process according to claim 24, wherein each element of the multiplexer is signed before multiplexing by a frequency marker applied on the phase.

34. (Previously Presented) The process according to claim 24, wherein each element of the multiplexer is signed before multiplexing by a frequency marker applied on the amplitude.

35. (Previously Presented) The process according to claim 34, wherein the marker comprises a signal with a predetermined spectrum.

36. (Previously Presented) The process according to claim 34, wherein the marker comprises a signal with a spectrum whose characteristics are a function of disturbances undergone by the signal on a corresponding path.

37. (Previously Presented) The process according to claim 34, wherein characteristics of the marker are determined to disturb a marked signal in such a manner that marking is evanescent during passage through the gate.

38. (Currently Amended) An apparatus for transmitting data on an optical fiber comprising a plurality of monochrome transmitters, each of which has its own transmission wavelength, with each transmitter having a local slave clock;

a multiplexer for wavelength division multiplexing signals coming from said plurality of monochrome transmitters;

an optical gate that comprises an electro-optical modulator and a clock and that receives multiplexed non-return to zero (NRZ) formatted signals and that reformats said multiplexed non-return to zero (NRZ) formatted signals to multiplexed return to zero (RZ) signals or to multiplexed carrier suppression return to zero (CS-RZ) signals, said reformatting being common and simultaneous for all carriers in the multiplexed signal; and

a synchronization circuit comprising a master clock controlling the clock of said optical gate and each slave local clock with a phase locked loop which supplies the synchronization signal for each of the transmitters.

39. (Cancelled)

40. (Previously Presented) The apparatus according to claim 38, further comprising frequency marking circuits for each element of the multiplex.

41. (Previously Presented) The apparatus according to claim 40, wherein each of the frequency marking circuits applies the marking signal onto one of the transmitters.

42. (Previously Presented) The apparatus according to claim 40, wherein each of the frequency marking circuits applies the marking signal onto synchronizer of each path.

43. (Previously Presented) The apparatus according to claim 38, wherein the optical gate comprises a detector for each marker to control characteristic of the formatting and adjustment of the phase of a corresponding path.

44. (Previously Presented) The apparatus according to claim 38, wherein the optical gate comprises a spectral analyzer for the marker to adjust the phase of each path.

45. (Previously Presented) The apparatus according to claim 38, further comprising an optical converter, a demultiplexer and a clock connected to at least one of the converters.

46. (Currently Amended) A counter-reaction circuit for an apparatus that transmits data on an optical fiber comprising a plurality of monochrome transmitters, each of which has its own transmission wavelength, with each transmitter having a local slave clock; an optical gate that comprises an electro-optical modulator and a clock and that receives multiplexed non-return to zero (NRZ) formatted signals and that reformats said multiplexed non-return to zero (NRZ) formatted signals to multiplexed return to zero (RZ) signals or to multiplexed carrier suppression return to zero (CS-RZ) signals; and an automatic controller of each transmitter phase comprising a master clock and a phase locked loop (PLL), said master clock controlling the clock of said optical gate and each slave local clock by using said phase locked loop which supplies the synchronization signal for each of the transmitters, wherein said counter-reaction circuit generates a frequency marker for injecting a disturbing spectral signal of each transmitter and further comprises a detector of an output signal from the optical gate which acts on the automatic controller of each transmitter phase to produce a selected spectral transformation of each marker.